

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : William Dally et al.                      Art Unit : 2782  
Serial No. : 09/637,500                                  Examiner : Henry Tsai  
Filed : August 11, 2000  
Title : HIGH-PERFORMANCE DIGITAL SIGNAL PROCESSOR (DSP) HAVING  
RISC INSTRUCTION EXECUTION PIPELINES

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	19	-	20	=	0	\$0
Independent	3	-	3	=	0	\$0
First Presentation of Multiple Dependent Claims						\$0

Applicant hereby petitions under 37 C.F.R. §1.136 for a 3 month extension of time.

\$1020

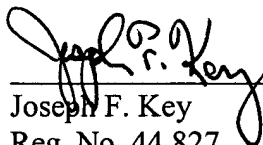
TOTAL FEE DUE

\$1020

A check for \$1020 is attached.

Respectfully submitted,

Date: 3/8/2005

  
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Joseph F. Key  
Reg. No. 44,827

Fish & Richardson P.C.  
1425 K Street, N.W.  
11th Floor  
Washington, DC 20005-3500  
Telephone: (202) 783-5070  
Facsimile: (202) 783-2331

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